

Claims

[c1] What is claimed is:

1. A printed circuit inductor comprising:

a first wiring layer;

a second wiring layer formed under and parallel to the first wiring layer;

a first conductive trace formed on the first wiring layer;

a second conductive trace formed on the second wiring layer;

a third conductive trace formed on the first wiring layer and parallel to the first conductive trace;

a fourth conductive trace formed on the second wiring layer and parallel to the second conductive trace;

a first via plug connected to a first end of the first conductive trace and a first end of the second conductive trace;

a second via plug connected to a second end of the second conductive trace and a first end of the third conductive trace; and

a third via plug connected to a second end of the third conductive trace and a first end of the fourth conductive trace.

- [c2] 2.The inductor of claim 1 wherein the first via plug is perpendicular to the first conductive trace, the second via plug is perpendicular to the second conductive trace, and the third via plug is perpendicular to the third conductive trace.
- [c3] 3.A printed circuit inductor comprising:
a plurality of conductive layers, wherein each conductive layer comprises a plurality of conductive traces and the conductive traces are formed alternately;
a plurality of insulating layers for isolating the conductive layers from each other; and
a plurality of via plugs for connecting the conductive traces on different conductive layers.
- [c4] 4.The inductor of claim 3 wherein the plurality of conductive layers is formed having two layers.
- [c5] 5.The inductor of claim 3 wherein the plurality of via plugs is perpendicular to the plurality of conductive layers.
- [c6] 6.The inductor of claim 3 wherein the magnetic field generated by the inductor is in parallel with the conductive layers.